



MODULE SPECIFICATION

Part 1: Information			
Module Title	Digital Systems Project		
Module Code	UFCFXK-30-3	Level	Level 6
For implementation from	2019-20		
UWE Credit Rating	30	ECTS Credit Rating	15
Faculty	Faculty of Environment & Technology	Field	Computer Science and Creative Technologies
Department	FET Dept of Computer Sci & Creative Tech		
Module type:	Project		
Pre-requisites	None		
Excluded Combinations	None		
Co- requisites	None		
Module Entry requirements	None		

Part 2: Description
<p>Features: Module Entry Requirements: 90 credits at level 2</p> <p>Educational Aims: This is an individual project. It provides the opportunity for the student to learn independently, and to develop and apply the skills necessary for an extended technical project.</p> <p>Outline Syllabus: Students select and investigate a topic beyond the normal level of treatment in the taught modules, resulting in a hardware and/or software artefact. The subject of the project will be agreed between the student and the supervisor, and may stem from a variety of sources; for example, a member of staff, the student, the student's employer or from an outside organisation. It must involve research, followed by the development of a hardware and/or software artefact using appropriate method(s)/tool(s). Whatever the subject, the student will be expected to treat material critically, to demonstrate their understanding of the relevance of material and to reflect upon the tools and methodologies used</p> <p>Teaching and Learning Methods: Each student will identify (or be assigned to) a supervisor who will meet regularly with the student to help plan and manage the work. Wherever possible students will be assigned a supervisor with an interest in the project topic, but this cannot be guaranteed. The responsibilities of the supervisor are primarily to provide guidance on the management of the project, the standard of work required, what can realistically be achieved in</p>

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the available time and to give feedback on work done (including the writing of the report).

At the beginning of the academic year in which the project is undertaken, a short series of lectures will provide the student with the context in which the project is to be undertaken, addressing areas such as choosing a project topic, researching the project idea, making use of your supervisor and writing up the project.

In the initial stages of the project, the student and their tutor will discuss objectives that must be achieved and appropriate scope for the project. Projects develop unpredictably, the initial objectives are only intended as a guide to the level expected and details may change. The student and supervisor will meet regularly throughout the duration of the project; the student is expected to stay in contact with and to make use of their supervisor.

The student will submit a research poster mid-way during the academic year, and will present this at a poster session. This poster will present the student's background research, recommendations for their product and key development directions. The student may also produce a prototype at this session. This session will provide the opportunity for the student to receive feedback from other students, and from staff.

Scheduled learning therefore includes lectures, project supervision and the poster session. Independent learning includes hours engaged in activities such as essential research, the development of requirements, design, programme code, programme testing and debugging, preparation and completion of the project report.

Part 3: Assessment

The assessment for this module has three components, a written report, a viva and a poster.

The written report provides the opportunity for students to communicate the work they have undertaken during the project, including research, the selection and implementation of appropriate approaches/methods, and the construction of the hardware and/or software artefact. Students will be expected to demonstrate a critical and reflective approach throughout. The viva provides the opportunity for the student to discuss their project with the supervisor and the second marker, to respond to questioning, and to demonstrate the developed artefact. The viva also provides the opportunity for the supervisor and second marker to assess the quality of the product.

Students will attend and present a research poster at a session held at the midpoint of the academic year. This aspect of the assessment is included in order to encourage student engagement. The poster will typically include the project objectives, the process the student is undertaking and progress so far. This session will provide the opportunity for students to obtain feedback from their peers, and from staff. It is expected that students will individually discuss this feedback with their supervisor, using it to inform the future direction of the project, and the development of the required artefact.

First Sit Components	Final Assessment	Element weighting	Description
Report - Component A		70 %	Project report (8000-10000 words)
Poster - Component A		5 %	Project-in-progress poster
Presentation - Component A	✓	25 %	Viva including demonstration held during exam period
Resit Components	Final Assessment	Element weighting	Description
Report - Component A		70 %	Reworked project report (8000-10000 words)
Presentation - Component A	✓	30 %	Viva including demonstration held during resit exam period

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Part 4: Teaching and Learning Methods																	
Learning Outcomes	<p>On successful completion of this module students will achieve the following learning outcomes:</p> <table border="1"> <thead> <tr> <th style="text-align: left;">Module Learning Outcomes</th> <th style="text-align: left;">Reference</th> </tr> </thead> <tbody> <tr> <td>Independently research a comprehensive body of knowledge in a chosen subject</td> <td>MO1</td> </tr> <tr> <td>Develop a hardware/software artefact by selecting appropriate approaches/methods for its realisation and construction</td> <td>MO2</td> </tr> <tr> <td>Identify and communicate knowledge of the development approaches/methods and their application.</td> <td>MO3</td> </tr> <tr> <td>Demonstrate analytical, critical and reflective skills.</td> <td>MO4</td> </tr> <tr> <td>Demonstrate informed reporting skills via research and critical valuation of appropriate academic, commercial and anecdotal literature</td> <td>MO5</td> </tr> </tbody> </table>	Module Learning Outcomes	Reference	Independently research a comprehensive body of knowledge in a chosen subject	MO1	Develop a hardware/software artefact by selecting appropriate approaches/methods for its realisation and construction	MO2	Identify and communicate knowledge of the development approaches/methods and their application.	MO3	Demonstrate analytical, critical and reflective skills.	MO4	Demonstrate informed reporting skills via research and critical valuation of appropriate academic, commercial and anecdotal literature	MO5				
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Reading List	<p><i>The reading list for this module can be accessed via the following link:</i></p> <p>https://uwe.rl.talis.com/modules/ufcfk-30-3.html</p>																

Part 5: Contributes Towards	
This module contributes towards the following programmes of study:	