



MODULE SPECIFICATION

Part 1: Information			
Module Title	Designing and Developing Device Drivers		
Module Code	UFCFX4-15-3	Level	Level 6
For implementation from	2020-21		
UWE Credit Rating	15	ECTS Credit Rating	7.5
Faculty	Faculty of Environment & Technology	Field	Computer Science and Creative Technologies
Department	FET Dept of Computer Sci & Creative Tech		
Module type:	Standard		
Pre-requisites	None		
Excluded Combinations	None		
Co- requisites	None		
Module Entry requirements	None		

Part 2: Description
<p>Educational Aims: This module intends to give the students an in depth practical course in device driver development for general purpose operating systems. The work will be located in the academic field of operating systems development but the emphasis will be on the practical difficulties in implementing and maintaining system interfaces for heterogeneous and rapidly changing collections of devices. The relationship of devices to the various sub systems within operating systems will be explored and criticised.</p> <p>The interaction of devices and buses will be examined. The students will develop a practical appreciation of the subject through the laboratory work which will involve the students writing an OS device driver from scratch. Strategies for testing and debugging will be covered as well as documentation standards through either modification of kernel code or through supplemental systems.</p> <p>In addition the educational experience may explore, develop, and practise but not formally discretely assess the following:</p> <p>Understand the need to work effectively with colleagues within a team</p> <p>Outline Syllabus: Topics covered will include:</p>

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Operating Systems and device drivers :

OS models, HALs types of OS – monolithic, microkernel, distributed device drivers and file systems, devices and buses

Device driver internals:

Device driver models, interfacing to the OS, interrupt and polled devices, DMA, accessing and managing kernel memory

Device driver development:

Finding information on devices, creating a device driver from data sheets, partitioning the driver, testing, debugging and documenting, optimisation and performance

Device driver examples:

Device drivers and system initialisation, I2C, SPI I2S device driver, codec device drivers, network device drivers, graphics drivers

Teaching and Learning Methods: The course will be paced through lectures, with group practicals and individual assignments providing a broadening experience. The theoretical content, introduced in lectures, will be reviewed in seminars. Personal work time will be used for background reading, report writing and preparation for laboratories.

Part 3: Assessment

The students will be assessed through a mix of practical assignment tasks and an examination. The practical tasks are designed to be completed over the course of the module, rather than as a piece of increased effort near the end of the teaching. This approach is taken to ensure sustained student engagement and to allow the student to demonstrate their mastery of a number of practical skills.

The more theoretical aspects of the course are assessed in the exam.

Component A 2 hour exam.

Component B practical coursework.

First Sit Components	Final Assessment	Element weighting	Description
Practical Skills Assessment - Component B		50 %	Practical coursework
Examination (Online) - Component A	✓	50 %	Online Examination (2 hours) 24 hour window
Resit Components	Final Assessment	Element weighting	Description
Practical Skills Assessment - Component B		50 %	Practical coursework
Examination (Online) - Component A	✓	50 %	Online Examination (2 hours) 24 hour window

Part 4: Teaching and Learning Methods

Learning Outcomes On successful completion of this module students will achieve the following learning outcomes:

Module Learning Outcomes

Master the practical difficulties of implementing system interfaces for heterogeneous and rapidly changing collections of devices.

Reference

MO1

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	Understand the requirements and functionality of device drivers	MO2
	Recognise and manipulate the relationship between device drivers and operating systems	MO3
	Develop, including debugging, testing and documenting, a device driver	MO4
	Benchmark competing device drivers	MO5
Contact Hours	Independent Study Hours:	
	Independent study/self-guided study	114
	Total Independent Study Hours:	114
	Scheduled Learning and Teaching Hours:	
	Face-to-face learning	36
	Total Scheduled Learning and Teaching Hours:	36
	Hours to be allocated	150
	Allocated Hours	150
Reading List	<p>The reading list for this module can be accessed via the following link:</p> <p>https://uwe.rl.talis.com/index.html</p>	

Part 5: Contributes Towards

This module contributes towards the following programmes of study:

Electronic and Computer Engineering [Sep][FT][Frenchay][3yrs] BEng (Hons) 2018-19

Electronic and Computer Engineering [Sep][SW][Frenchay][4yrs] BEng (Hons) 2018-19

Electronic and Computer Engineering [Sep][PT][GlosColl][5yrs] BEng (Hons) 2018-19

Electronic and Computer Engineering {Apprenticeship} [Sep][PT][GlosColl][5yrs] BEng (Hons) 2018-19