



## **Module Specification**

### **Embedded Systems Security**

Version: 2026-27, v2.0, Approved

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## Part 1: Information

**Module title:** Embedded Systems Security

**Module code:** UFCFJU-30-2

**Level:** Level 5

**For implementation from:** 2026-27

**UWE credit rating:** 30

**ECTS credit rating:** 15

**College:** College of Arts, Technology and Environment

**School:** CATE School of Computing and Creative Technologies

**Partner institutions:** None

**Field:** Computer Science and Creative Technologies

**Module type:** Module

**Pre-requisites:** Networking 2025-26, Operating Systems and Architecture 2025-26

**Excluded combinations:** None

**Co-requisites:** None

**Continuing professional development:** No

**Professional, statutory or regulatory body requirements:** None

## Part 2: Description

**Overview:** This module aims to provide apprentices with an in-depth appreciation of embedded devices and their security.

An embedded system is a combination of processor, memory, I/O and the OS that forms a device.

Embedded systems get infrequent or never get software updates. They are very

many identical devices installed, often in critical facilities and systems. Because of this the devices must be made secure.

Embedded systems have different characteristics, ubiquity and vulnerabilities to desktop and server systems. Comparisons will be made in the module.

Delivery will cover modern system architecture, key technologies, and the security implications of implementing these technologies. In addition, essential general low-level malware techniques will be examined.

**Features:** Not applicable

**Educational aims:** Contributes to underpinning cyber knowledge and extends it into the field of embedded systems.

**Outline syllabus:** Topics may include, but are not limited to:

Architecture of low powered mobile systems

The nature of security in embedded and network systems

Networking technologies

Boot processes, BIOS, file systems and embedded operating systems

Interaction between microprocessor software and signals from sensors, actuators, etc

Exploitation of external environment or software-hardware interface and mitigations that may be employed

Security challenges of embedded systems, for example:  
size, power, processor, memory, bandwidth limitations

Internet of Things

Low level mechanisms used by current malware

Machine level instruction set

Reverse engineering techniques

Reverse engineering for malware analysis

De-obfuscation of obfuscated code

Anti-debugging mechanisms

### **Part 3: Teaching and learning methods**

**Teaching and learning methods:** Lecture sessions cover the technical knowledge required. Designated practical work is included to ensure that apprentices have absorbed and understood the key principles involved.

**Module Learning outcomes:** On successful completion of this module students will achieve the following learning outcomes.

**MO1** Describe and explain the characteristics and complexity of secure, embedded systems.

**MO2** Implement software for selected, novel, embedded devices that interact with the real world and analyse for security exploits.

**MO3** Analyse and evaluate security threats and vulnerabilities with regards to embedded systems and identify how these can be mitigated.

**MO4** Analyse malware and identify its mechanisms.

**Hours to be allocated:** 300

**Contact hours:**

Independent study/self-guided study = 135 hours

Placement = 75 hours

Face-to-face learning = 90 hours

**Reading list:** The reading list for this module can be accessed at [readinglists.uwe.ac.uk](https://uwe.rl.talis.com/index.html) via the following link <https://uwe.rl.talis.com/index.html>

## **Part 4: Assessment**

**Assessment strategy:** The following assessment applies at both first and resit.

A 30 minute presentation & Q&A session will allow students not only to demonstrate their technical knowledge of malware threats, engineering and the techniques required for analysis but also allow them to practice their communication skills. The Q&A session gives the students to think on their feet and also gives the chance to fill in any of the gaps that they may have left in their presentation. on malware threats, engineering and the techniques required for analysis

Students for also complete a series of tasks during classroom time. These tasks will form the basis of a workbook which will be presented for assessment. The tasks will challenge them to develop independent skills in using and securing embedded systems whilst still allowing them plenty of support in what is likely to be a curriculum area that is very new to them.

### **Assessment tasks:**

#### **Presentation (First Sit)**

Description: 30 minute presentation and Q&A session.

Weighting: 40 %

Final assessment: Yes

Group work: No

Learning outcomes tested: MO3, MO4

#### **Practical Skills Assessment (First Sit)**

Description: Series of classroom-based tasks involving using and securing embedded systems. The tasks will be recorded in a workbook, signed off and submitted for assessment.

Weighting: 60 %

Final assessment: No

Group work: No

Learning outcomes tested: MO1, MO2, MO3

### **Presentation (Resit)**

Description: 30 minute presentation and Q&A session.

Weighting: 40 %

Final assessment: Yes

Group work: No

Learning outcomes tested: MO3, MO4

### **Practical Skills Assessment (Resit)**

Description: Series of classroom-based tasks involving using and securing embedded systems. The tasks will be recorded in a workbook, signed off and submitted for assessment.

Weighting: 60 %

Final assessment: No

Group work: No

Learning outcomes tested: MO1, MO2, MO3

## **Part 5: Contributes towards**

This module contributes towards the following programmes of study:

Cyber Security Technical Professional {Apprenticeship-GLOSCOLL} [GlosColl] BSc (Hons) 2025-26