



MODULE SPECIFICATION

Part 1: Information			
Module Title	Practical Electronics		
Module Code	UFMFCA-15-1	Level	Level 4
For implementation from	2020-21		
UWE Credit Rating	15	ECTS Credit Rating	7.5
Faculty	Faculty of Environment & Technology	Field	Engineering, Design and Mathematics
Department	FET Dept of Engin Design & Mathematics		
Module type:	Project		
Pre-requisites	None		
Excluded Combinations	None		
Co- requisites	None		
Module Entry requirements	None		

Part 2: Description
<p>Educational Aims: In addition to the educational aims the educational experience may explore, develop, and practise but not assess the following:</p> <p>Working as a team member Printed circuit board design Circuit simulation</p> <p>Outline Syllabus: Use of basic laboratory instrumentation (PSU, oscilloscope, DMM, function generator).</p> <p>Ohm's law - Resistors and resistance (parallel and series), value marking, tolerance.</p> <p>Diode – basic operation and V-I characteristics.</p> <p>Rectification and smoothing.</p> <p>Transistor operation, as an amplifier and as a switch.</p> <p>Operational Amplifiers.</p>

STUDENT AND ACADEMIC SERVICES

Digital Logic ICs.

AC waveforms, RMS.

Project work will encompass more advanced topics, e.g. battery charging, timing circuits, dc-dc converters etc.

Teaching and Learning Methods: The module will mainly involve laboratory sessions of a directed nature; the broad aim of this approach is to underpin/reinforce theoretical knowledge gained either prior to or after joining the course, in order to ensure a minimum standard of practical ability is achieved across a cohort with potentially diverse backgrounds.

The primary focus will be to introduce students to a number of common electronic components in a practical setting, and to provide the stimulus to investigate the characteristics and performance of circuits built from these components.

Students will be encouraged to use private study time to investigate the theoretical principles on which the laboratory exercises are based, and to gain familiarity with the components encountered by studying the associated data sheets which are available on-line.

Students will also gain experience in the use of standard test equipment, and gain confidence through the use of such equipment 'in context'.

The keeping of formal laboratory notebooks will be required to ensure adequate recording of the work undertaken, and to satisfy the requirements of accrediting bodies such as the IET that such practice is observed. The laboratory notebook will also serve as the primary grading mechanism for summative assessment, and as a vehicle for providing feedback on work undertaken across a variety of topics.

To integrate the practical concepts, students will also be required to work in groups for a design and build project: Each group will need to submit a project report that will include an individual reflective statement from each team member. Students are required to manage their own time during the entire module so as to work on individual practical exercises and the Group Project which run concurrently. The teaching/learning format for the Group Project is at least partly EBL (enquiry-based learning), where the students are directed to request / demand key information in order to be able to meet the brief.

Contact hours: 36

Self-study: 50

Assignment Preparation: 64

Examination Preparation: 0

Total: 150

Part 3: Assessment

Assessment for the module consists of an individual project. It will involve the design, (building) and test of a system for which they have to submit a Individual Report (1500 words) (100%).

First Sit Components	Final Assessment	Element weighting	Description
Project - Component A	✓	100 %	Individual project (1500 word report)
Resit Components	Final Assessment	Element weighting	Description
Project - Component A	✓	100 %	Individual project (1500 word report)

STUDENT AND ACADEMIC SERVICES

Part 4: Teaching and Learning Methods																	
Learning Outcomes	<p>On successful completion of this module students will achieve the following learning outcomes:</p> <table border="1"> <thead> <tr> <th style="text-align: left;">Module Learning Outcomes</th> <th style="text-align: left;">Reference</th> </tr> </thead> <tbody> <tr> <td>Demonstrate knowledge of and competence in the use of basic laboratory instrumentation</td> <td>MO1</td> </tr> <tr> <td>Demonstrate knowledge and understanding of a number of common electronic components</td> <td>MO2</td> </tr> <tr> <td>Show cognitive skills with respect to the analysis of a design specification, and make appropriate component selection through research and analysis of available component data</td> <td>MO3</td> </tr> <tr> <td>Demonstrate knowledge of and competence in the use of standard electronic hardware prototyping techniques</td> <td>MO4</td> </tr> <tr> <td>Show cognitive skills with respect to the analysis of a requirement specification or problem definition, and synthesize a design solution based on appropriate research and the application of acquired skills and experience</td> <td>MO5</td> </tr> <tr> <td>Demonstrate knowledge and understanding of the need to keep adequate experimental notes</td> <td>MO6</td> </tr> </tbody> </table>	Module Learning Outcomes	Reference	Demonstrate knowledge of and competence in the use of basic laboratory instrumentation	MO1	Demonstrate knowledge and understanding of a number of common electronic components	MO2	Show cognitive skills with respect to the analysis of a design specification, and make appropriate component selection through research and analysis of available component data	MO3	Demonstrate knowledge of and competence in the use of standard electronic hardware prototyping techniques	MO4	Show cognitive skills with respect to the analysis of a requirement specification or problem definition, and synthesize a design solution based on appropriate research and the application of acquired skills and experience	MO5	Demonstrate knowledge and understanding of the need to keep adequate experimental notes	MO6		
Module Learning Outcomes	Reference																
Demonstrate knowledge of and competence in the use of basic laboratory instrumentation	MO1																
Demonstrate knowledge and understanding of a number of common electronic components	MO2																
Show cognitive skills with respect to the analysis of a design specification, and make appropriate component selection through research and analysis of available component data	MO3																
Demonstrate knowledge of and competence in the use of standard electronic hardware prototyping techniques	MO4																
Show cognitive skills with respect to the analysis of a requirement specification or problem definition, and synthesize a design solution based on appropriate research and the application of acquired skills and experience	MO5																
Demonstrate knowledge and understanding of the need to keep adequate experimental notes	MO6																
Contact Hours	<table border="1"> <thead> <tr> <th colspan="2">Independent Study Hours:</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">Independent study/self-guided study</td> <td style="text-align: center;">114</td> </tr> <tr> <td style="text-align: center;">Total Independent Study Hours:</td> <td style="text-align: center;">114</td> </tr> <tr> <th colspan="2">Scheduled Learning and Teaching Hours:</th> </tr> <tr> <td style="text-align: center;">Face-to-face learning</td> <td style="text-align: center;">36</td> </tr> <tr> <td style="text-align: center;">Total Scheduled Learning and Teaching Hours:</td> <td style="text-align: center;">36</td> </tr> <tr> <td style="text-align: center;">Hours to be allocated</td> <td style="text-align: center;">150</td> </tr> <tr> <td style="text-align: center;">Allocated Hours</td> <td style="text-align: center;">150</td> </tr> </tbody> </table>	Independent Study Hours:		Independent study/self-guided study	114	Total Independent Study Hours:	114	Scheduled Learning and Teaching Hours:		Face-to-face learning	36	Total Scheduled Learning and Teaching Hours:	36	Hours to be allocated	150	Allocated Hours	150
Independent Study Hours:																	
Independent study/self-guided study	114																
Total Independent Study Hours:	114																
Scheduled Learning and Teaching Hours:																	
Face-to-face learning	36																
Total Scheduled Learning and Teaching Hours:	36																
Hours to be allocated	150																
Allocated Hours	150																
Reading List	<p><i>The reading list for this module can be accessed via the following link:</i></p> <p>https://uwe.rl.talis.com/modules/ufmfca-15-1.html</p>																

Part 5: Contributes Towards

This module contributes towards the following programmes of study:

Electronic and Computer Engineering [Sep][PT][GlosColl][5yrs] BEng (Hons) 2019-20

Electronic and Computer Engineering {Apprenticeship} [Sep][PT][GlosColl][5yrs] BEng (Hons) 2019-20

Robotics {Foundation} [Sep][FT][Frenchay][4yrs] BEng (Hons) 2019-20

Robotics {Foundation} [Sep][SW][Frenchay][5yrs] BEng (Hons) 2019-20

Instrumentation and Control Engineering {Foundation} [Oct][PT][GCET][8yrs] BEng (Hons) 2018-19

Instrumentation and Control Engineering {Foundation} [Feb][PT][GCET][8yrs] BEng (Hons) 2018-19